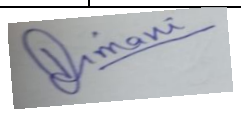
Lesson Plan

Name of the Teacher: Himani Vaidya **Subject** : Digital System Applications

Semester: 3rd Semester Comp Engg **Session:** Sep,22 to Jan,23

Month: September

Sr. No.	Week	Date	Name of the Chapter	Contents to be taught	Remark
1	1	1st,2nd	**Chapter1**. Introduction to Digital Systems	➢ Analog System: Analog Signal , Graphical representation,Examles and Disadntages. ➢ Digital Systems: Digital Signal, graphical representation , example, advantages and limitation of digital systems.	
2	2	5th, 7th 8th 9th	**Chapter1**. Introduction to Digital Systems **Chapter 2**. Digital Number System and their Conversion	➢ Comparison of analog and digital Systems ➢ Introduction to Analog to digital convertor and digital to analog convertor. ➢ Binary Number System: Characteristics. Binary to Decimal Conversion. Decimal to Binary Conversions	
3	3	12th ,14th, 15th, 16th	**Chapter 2**. Digital Number System and their Conversion	➢ Signed Binary Number Signed Magnitude Representation ➢ One's Complement and 2's Compliment representation ➢ Octal Number System: Characteristics. Octal to Decimal, Decimal to Octal, octal to binary, and binary to octal number System.	
4	4	19th, 21st, 22nd 23rd	**Chapter 2**. Digital Number System and their Conversion	➢ Hexadecimal Number System:Characteristics . Hexadecimal to Decimal	
5	5	26th , 28th, 29th ,30th	**Chapter 2**. Digital Number System and their Conversion **Chapter 3**. Binary Arithmetics	➢ , Decimal to Hexadecimal, Hexadecimal to octal, octal to hexadecimal and binary to hexadecimal , hexadecimal to binary conversions ➢ Binary Arithmetics: Binary Addition , Binary Subtraction, Binary multiplication, Binary Division.	



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Month: October

Sr. No.	Week	Date	Name of the Chapter	Contents to be taught	Remark
6	1	3rd, 6th, 7th	**Chapter 3**. Binary Arithmetics	➢ Addion and Subtraction using two's compliment Representation, ➢ Octal Arithmetics: Octal addition and octal Subtraction. ➢ Hexadecimal Arithmetic: Hexadecimal Addition and Hexadecimal Subtraction.	
7	2	10th, 12th, 13th, 14th.	**Chapter 4**. Boolean Algebra and Logics Gates	➢ Boolean Algebra: Axioms of Boolean Algebra. ➢ Boolean Laws: Commutative,associative laws and Distributive laws. ➢ AND ,OR and Inversion Laws. ➢ **1st CLASS TEST**	
8	3	17th, 19th, 20th, 21st.	**Chapter 4**. Boolean Algebra and Logics Gates **Chapter 5**. Simplification of Boolean Functions	➢ Demorgans Theorems ➢ Logic Gates: And operation, NOT Operations, NAND operations and EX-OR operation. ➢ University of NAND and NOR Gates ➢ Boolean Function: Defination , Truth Table Formation. Standard	
9	4	27th, 28th, 31st	**Chapter 5**. Simplification of Boolean Functions	➢ K-Map Represenation of Boolean Functions. ➢ Simplification of Boolean Function using K-map (up to three Variables)	

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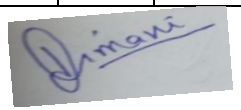
Month: November

Sr. No.	Week	Date	Name of the Chapter	Contents to be taught	Remark
10.	1	2nd , 3rd, 4th	**Chapter 5**. Simplification of Boolean Functions **Chapter 6**. Combinational Circuits	➢ Minimization of Boolean Function specified in Minterm,Maxterm and Truth Table. ➢ Half Adder Circuit ➢ Full adder Circuit ➢ Half Subtractor Circuit ➢ Full Subtractor Circuit	
11.	2	7th, 9th, 10th, 11th	**Chapter 6**. Combinational Circuits	➢ Multeplexer: 2:1,4:1,16:1. ➢ Demultiplexer: 1:4, 1:8,1:16 ➢ **2nd CLASS TEST**	
12.	3	14th , 16th, 17th, 18th	**Chapter 6**. Combinational Circuits	➢ Encoder ➢ Priority Encoder	
13.	4	21st ,23rd .24th , 25th	**Chapter 6**. Combinational Circuits **Chapter 7**. Flip Flop	➢ Decoder ➢ 2 to 4line decoder ➢ S-R Flip flop ➢ JK flip Flop: ➢ **House Tests**	
14.	5	28th, 30th	**Chapter 7**. Flip Flop	➢ Master Slave JK flip flop	



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Name of the Teacher: Himani Vaidya **Subject** : Digital System Applications

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Month: December

Sr. No.	Week	Date	Name of the Chapter	Contents to be taught	Remark
15.	1	1st, 2nd	**Chapter 7**. Flip Flop	➢ D Flip Flop ➢ T Flip Flop	
16.	2	5th, 7th, 8th, 9th	**Chapter 8** . Semiconductor Memory Devices	➢ RAM : Characteristics and Types ➢ ROM: its Type	
17.	3	12th,14th, 15th, 16th	**Chapter 8** . Semiconductor Memory Devices	➢ Flash memory: Characteristics and types. ➢ Revision	
18.	4	19th		➢ Revision	

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